

COVER

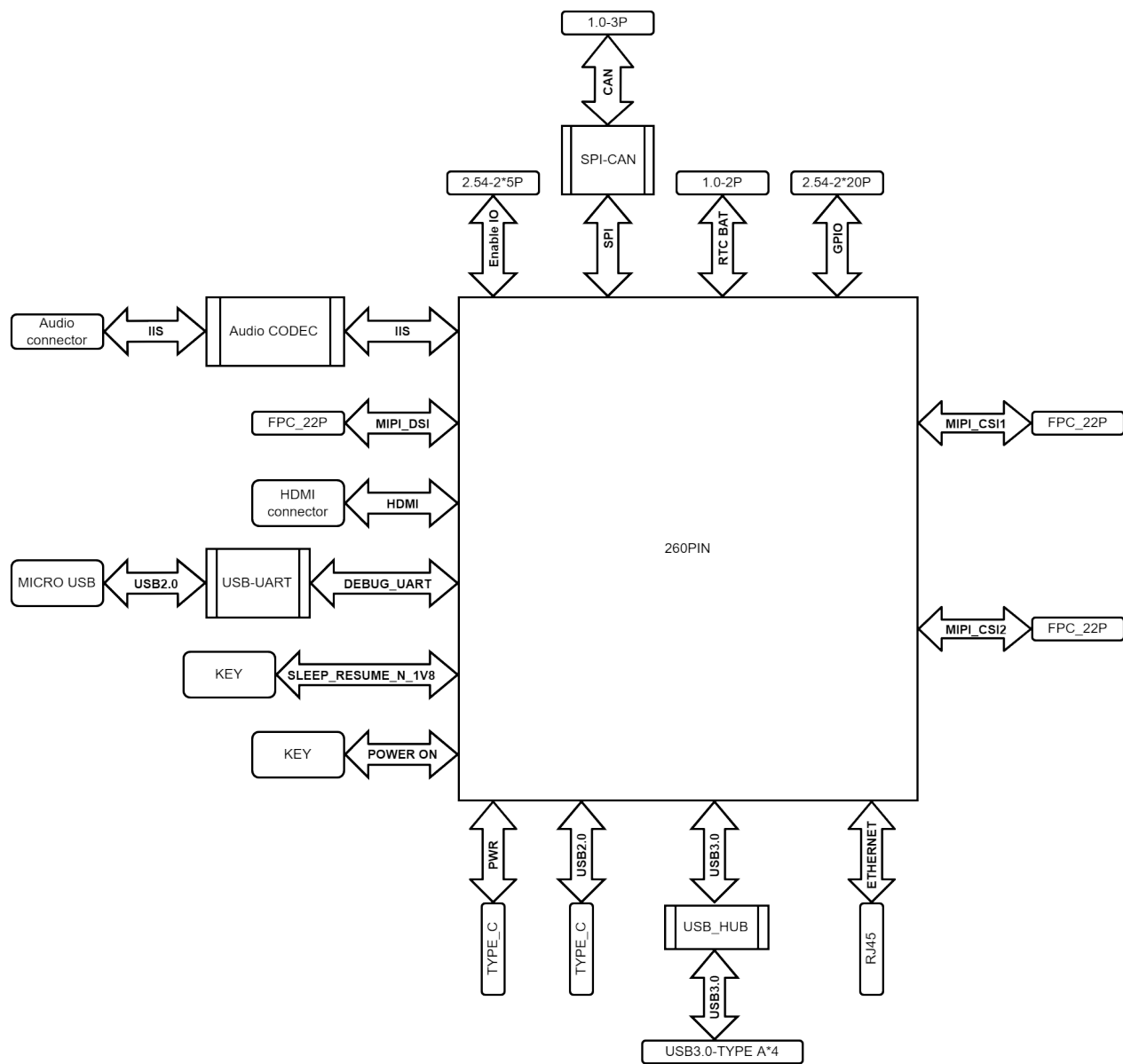
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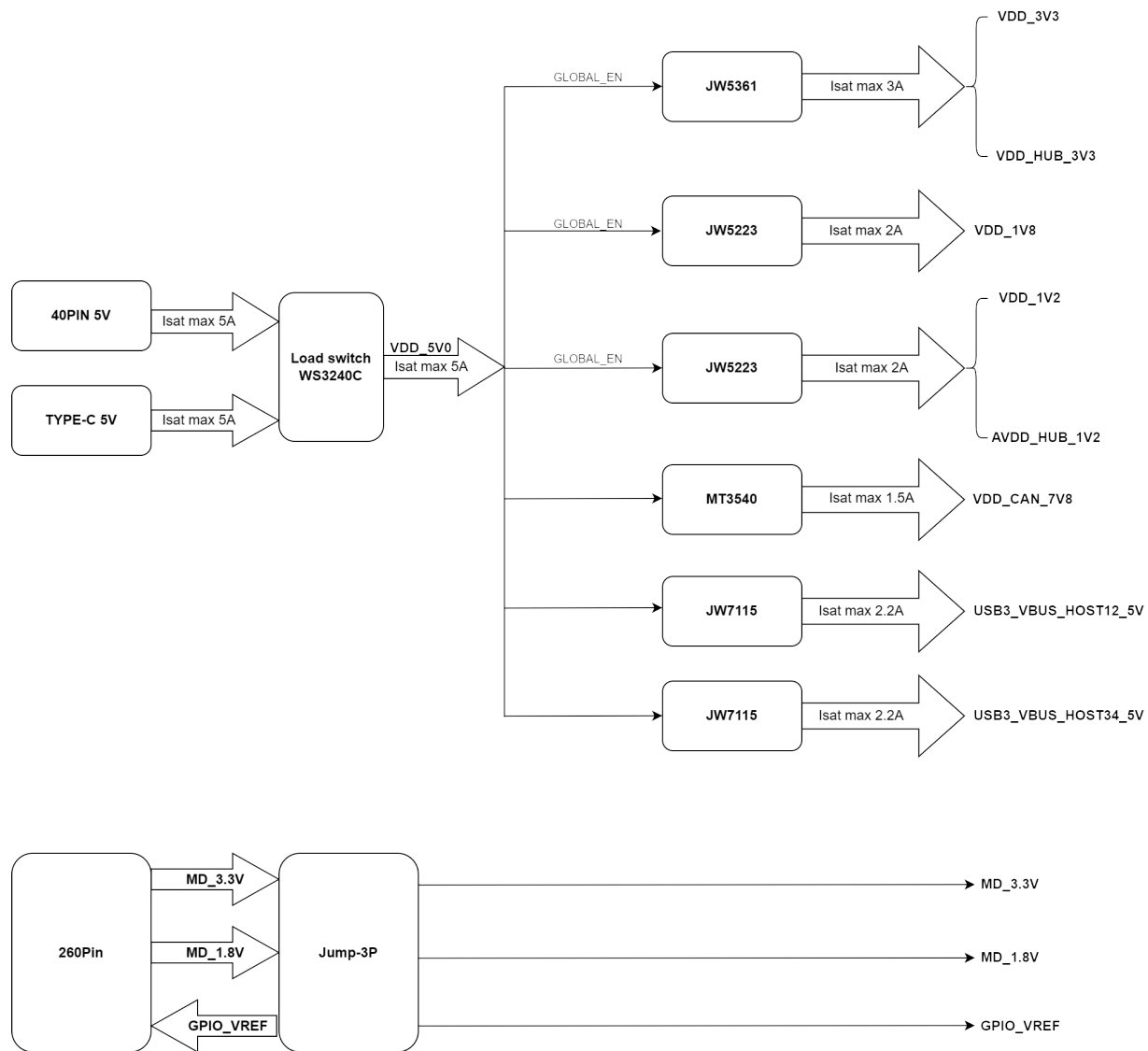
Revision History

Rev. Code	Description
V0.1	Initial release, under development and verification.
V0.2	The pin definitions of the 260-pin interface have been modified (P00234, 235, 232, 234, 236), which are incompatible with Version V0.1.
V0.3	Modify the USB2.0 HostDevice Selection design.
V1.0	Modify some pin definitions and make it compatible with EMMC startup.

BLOCK

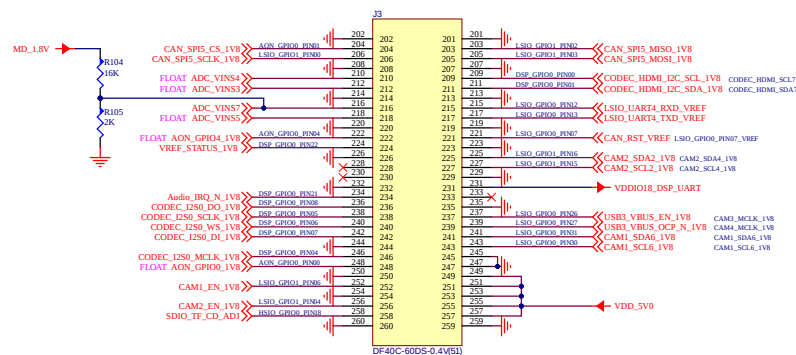
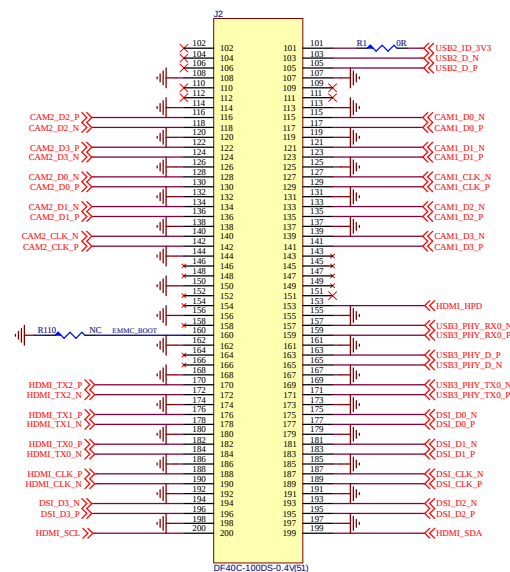
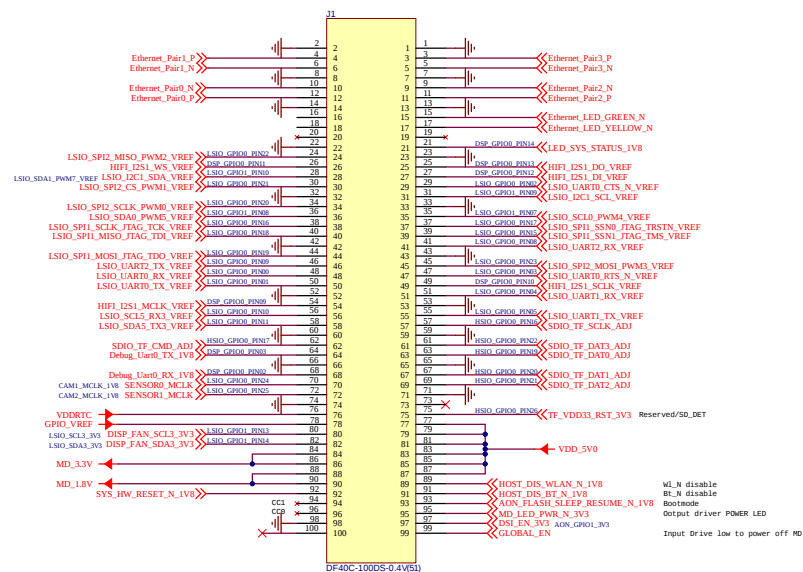


POWER_TREE



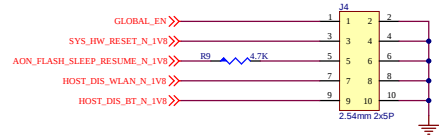
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Project:	RDK_X5_MD_Carrier_Board_V1P0		
File:	P03_POWER_TREE.SchDoc		
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Design by:	D-Robotics Hardware Team	Sheet:	3 of 13
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260 Pins Connector



Title:				
Project:	RDK_X5_MD_Carrier_Board_V1P0			
File:	P04_260PINS.SchDoc			
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Mode SEL



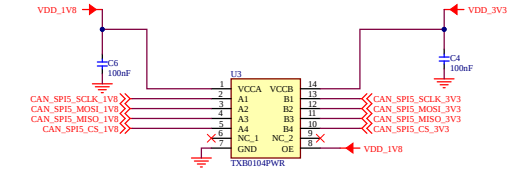
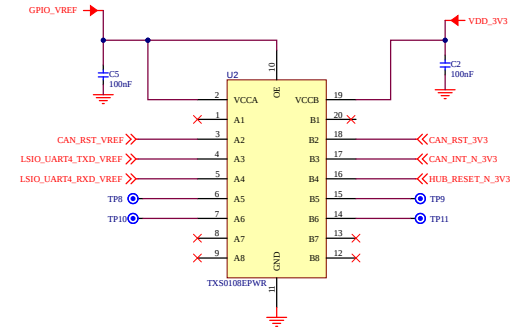
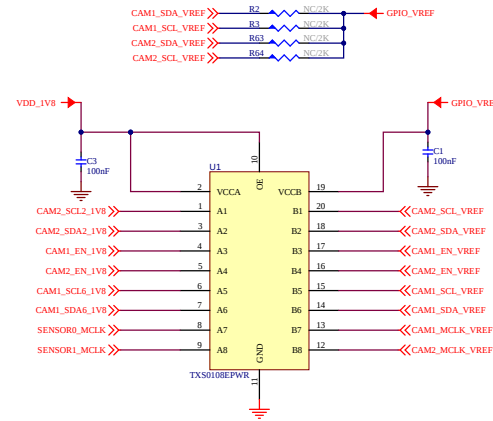
VDD_3V3 → R5 → NC/10K → SYS_JHW_RESET_N_IV8

VDD_5V0 → R6 → NC/10K → GLOBAL_EN

GLOBAL_EN is low, and all power to the module is turned off.

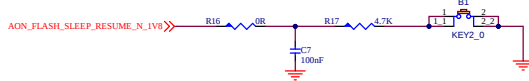
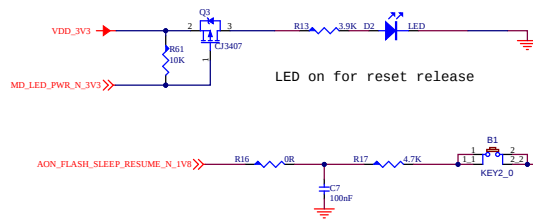
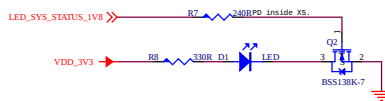
RUN_PG is low, providing an effective reset to the main chip.

IO Level Shifter



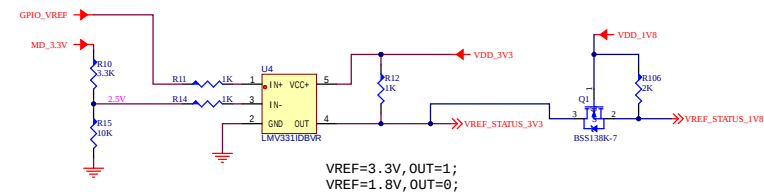
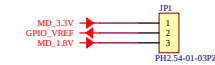
LED

Module Sate LED: running, flashing



VREF SEL

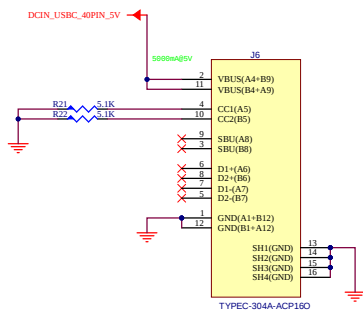
GPIO_VREF default 3V3, optional 1V8
(Requires 1V8 level software for the module)



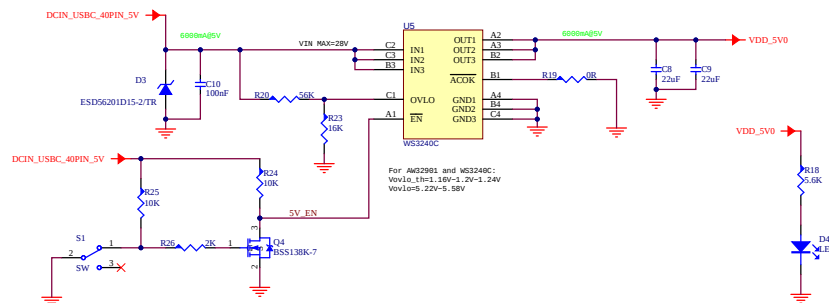
VREF=3.3V, OUT=1;
VREF=1.8V, OUT=0;

Title:	D-Robotics		
Project:	RDK_X5_MD_Carrier_Board_V1P0		
File:	P05_LED_LEVEL_SHIFTER.SchDoc		
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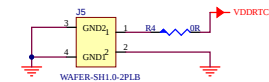
USB-C POWER (MAX 5A@5V)



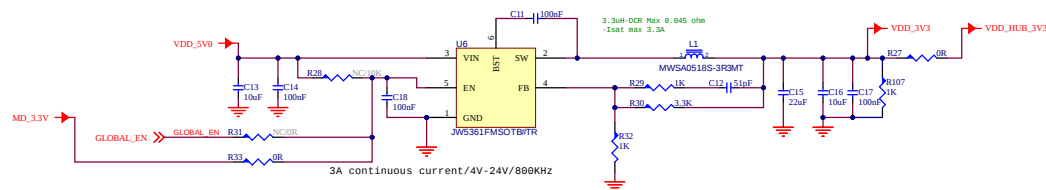
DC INPUT OVP



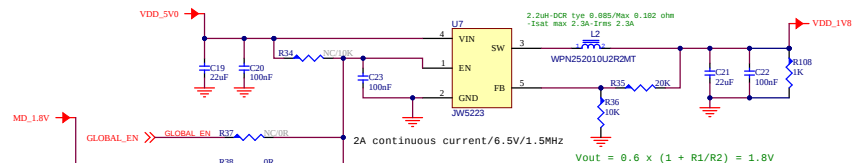
RTC BATT



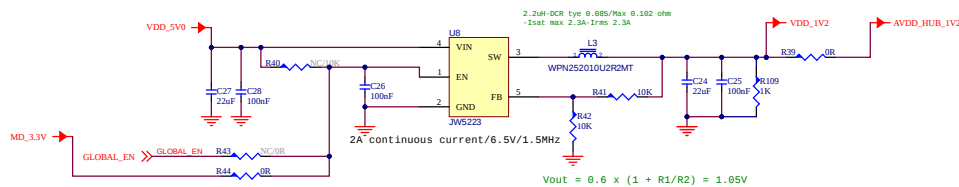
VDD_3V3



VDD_1V8

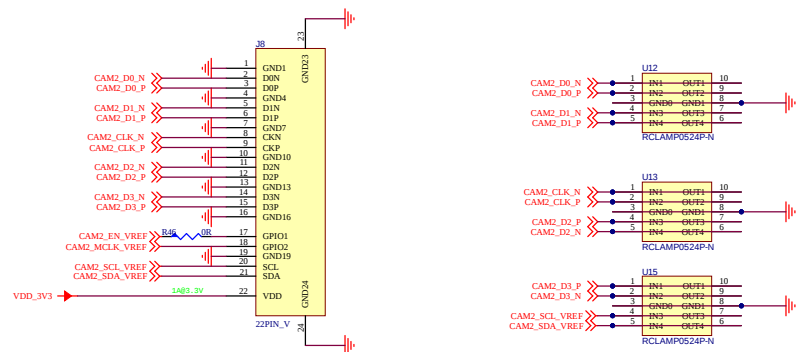
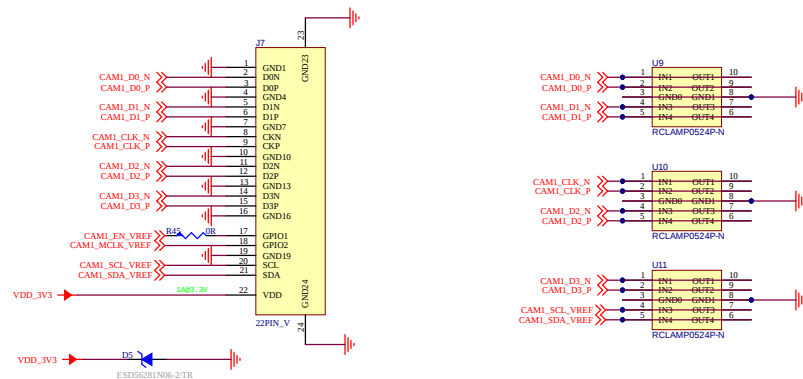


VDD_1V2

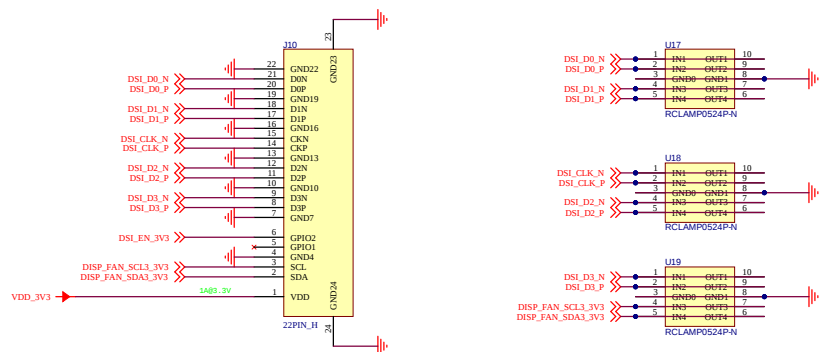


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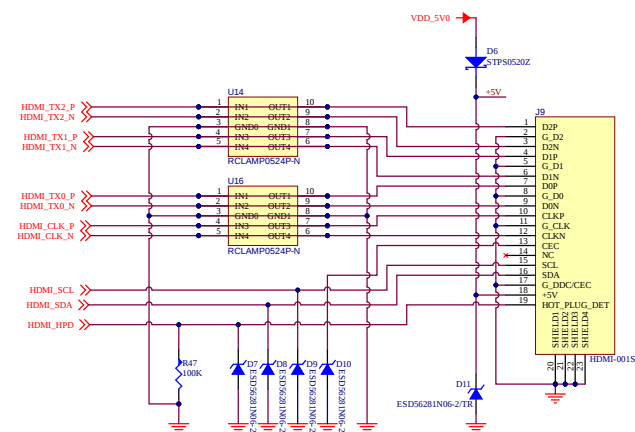
MIPI CSI



MIPI DSI

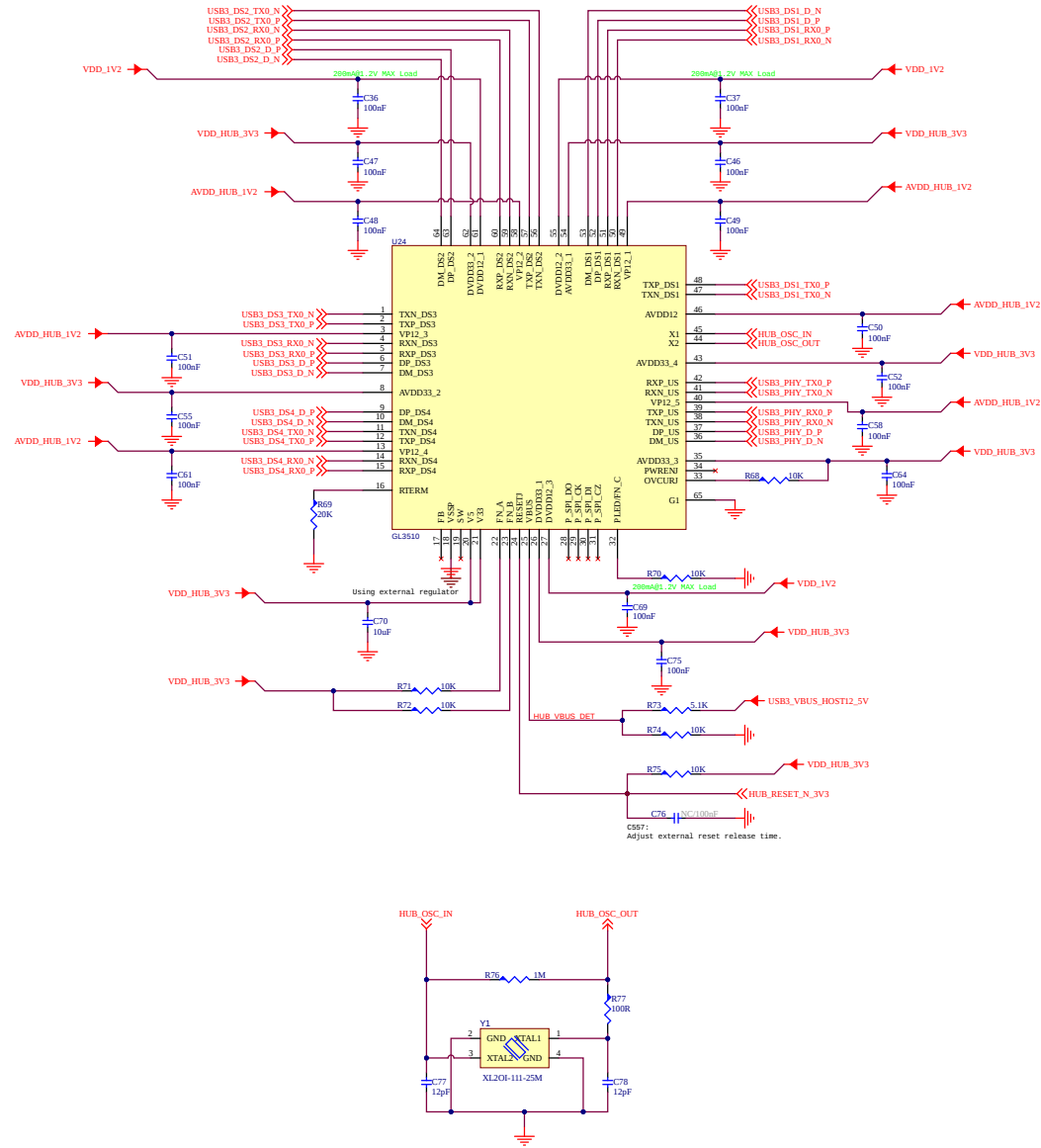


HDMI Type-A



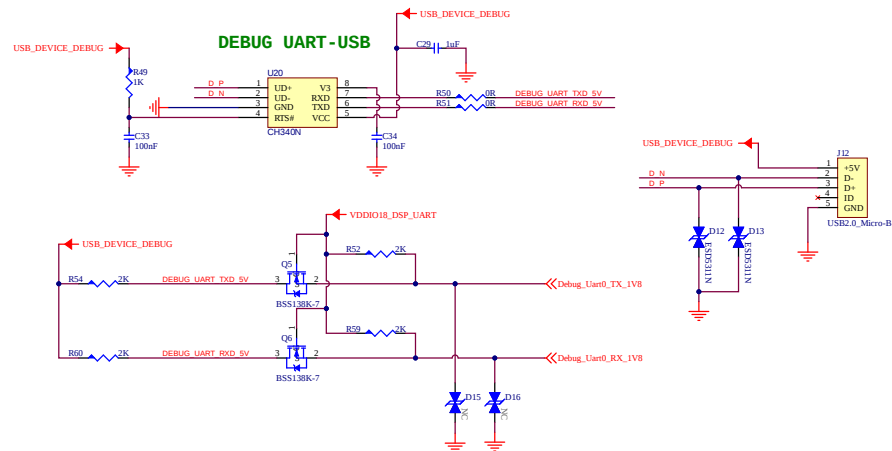
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File:	P07_MIP1_HDMI_SchDoc		
Page Size:	A2	Rev:	V1.0
Design by:	D-Robotics Hardware Team	Sheet:	7 of 13
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USB 3.0 HUB

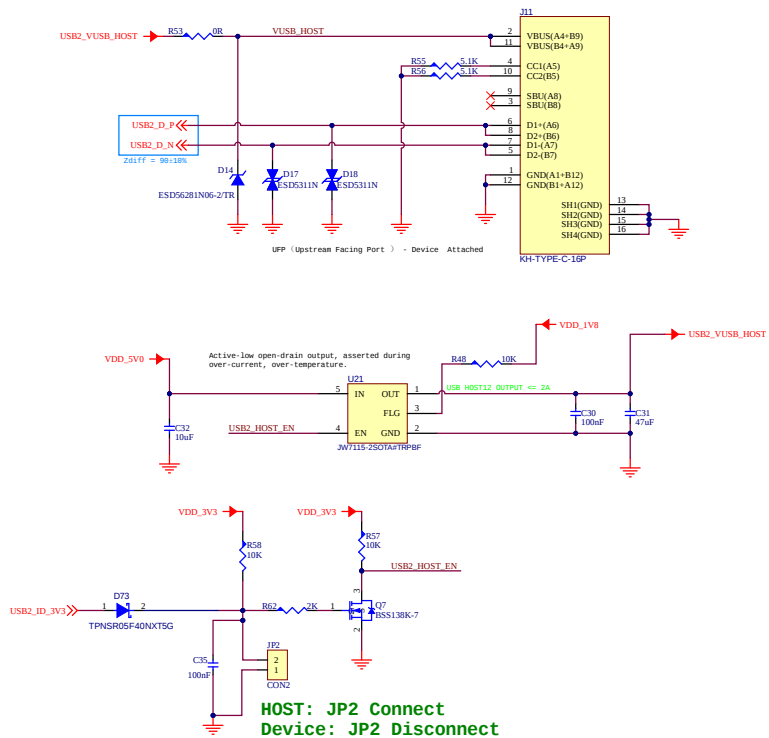


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Design by:	D-Robotics Hardware Team	Sheet:	8 of 13
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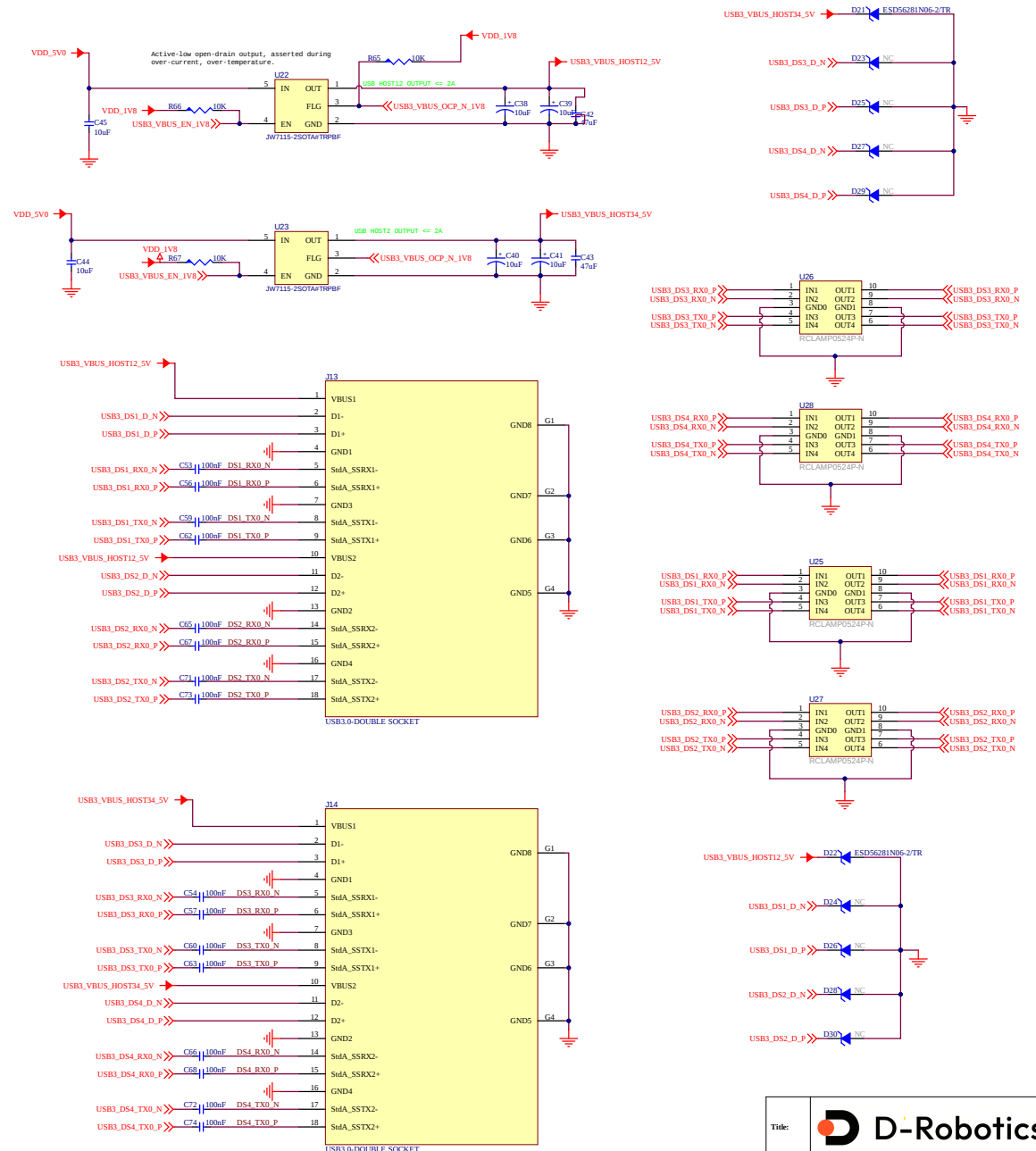
DEBUG UART MICRO-B



USB 2.0 DEVICE TYPE-C



USB 3.0 HOST TYPE-A



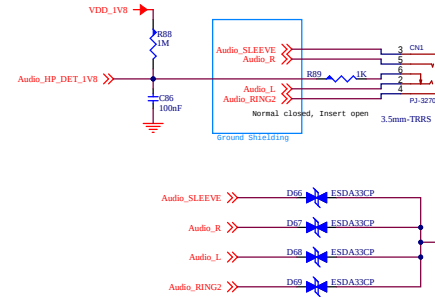
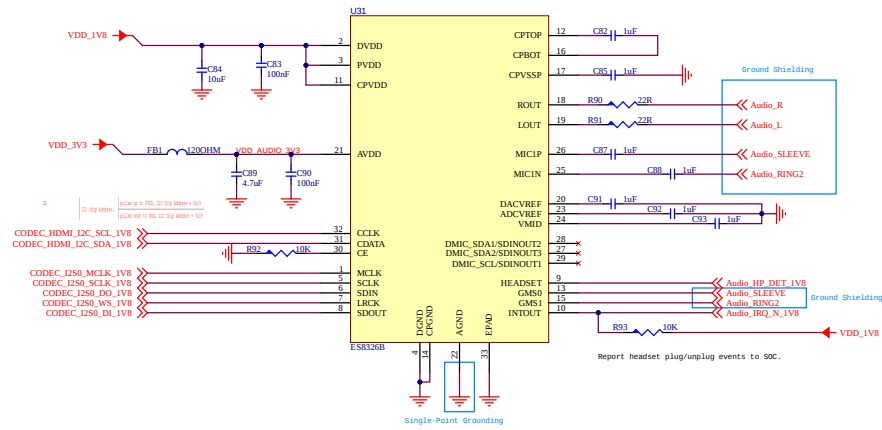
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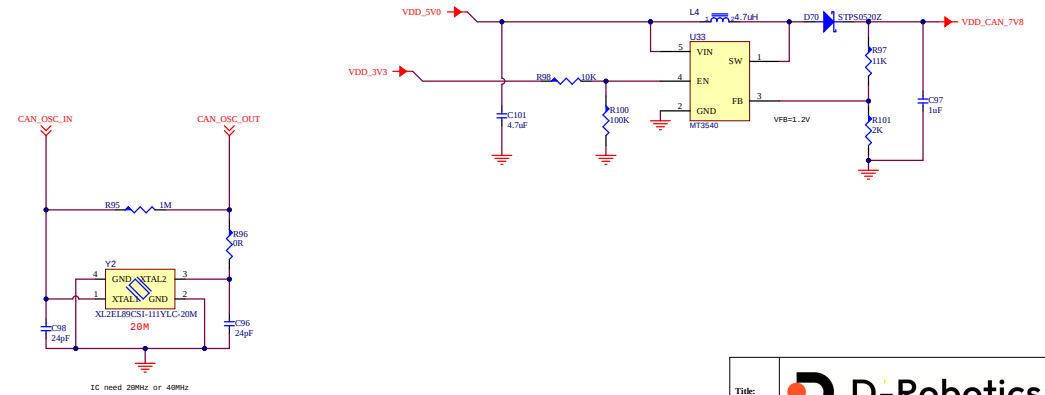
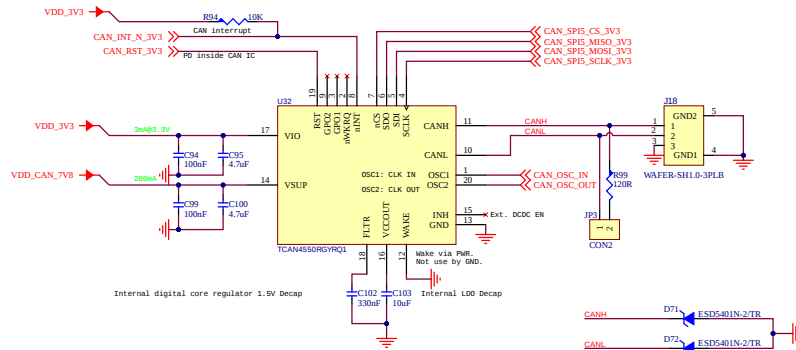
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AUDIO



CAN



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File:	P11_AUDIO_CAN_SchDoc		
Page Size:	A2	Rev:	V1.0
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A

D

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